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**United States Patent** [19]

Steere, Jr. et al.

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[54] **SYSTEM HAVING TWO PC CARDS IN A HINGED CARRYING CASE WITH BATTERY COMPARTMENT WITHIN IN THE HINGE SECTION**

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**Related U.S. Application Data**

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[51] **Int. Cl.<sup>6</sup>** ..... **G06F 13/00**

[52] **U.S. Cl.** ..... **395/882; 395/892; 395/893**

[58] **Field of Search** ..... 235/280, 472,  
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707, 708.1; 380/23; 395/893, 882, 892;  
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[56] **References Cited**

**U.S. PATENT DOCUMENTS**

4,814,591	3/1989	Nara et al.	235/280
5,233,547	8/1993	Kapp et al.	364/705.02
5,293,424	3/1994	Holtey et al.	380/23
5,335,557	8/1994	Yasutake	73/862.043
5,373,149	12/1994	Rasmussen	235/492
5,451,933	9/1995	Stricklin et al.	340/825.06
5,486,687	1/1996	Le Roux	235/382
5,500,517	3/1996	Cagliostro	235/486
5,521,369	5/1996	Kumar	235/472
5,522,089	5/1996	Kikinis et al.	395/893

**OTHER PUBLICATIONS**

D. Chaum, "Untraceable Electronic Mail, Return Address, and Digital Pseudonyms" Comm. of the ACM, 24(2), Feb. 1981, pp. 84-88.

A. Fujioka, "A Practical Secret Voting Scheme for Large Scale Elections", Adv. in Cryptology—Auscrypt '92, 1992, pp. 244-251.

C. Park, "Efficient Anonymous Channel and All/Nothing Election Scheme", Adv. in Cryptology—Auscrypt '93, pp. 248-259.

A. Fiat, "How to Prove Yourself: Practical Solutions to Identification and Signature Problems" Adv. in Cryptology—Crypto '86, Springer-Verlag, 1986, pp. 186-199.

B. Pfitzmann "Breaking an Efficient Anonymous Channel" Proc. Eurocrypt 94, pp. 339-348.

M. Naor "Bit commitment Using Pseudo-Randomness" in Adv. in Cryptology—Crypto '89, 1989, pp. 128-136.

Y. Desmedt, "Threshold Cryptosystems", Adv. in Cryptology—Crypto '89, 1989, pp. 307-315.

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[57] **ABSTRACT**

A companion computer system for a host computer wherein the host computer includes an interface for electrically and mechanically interfacing to the companion computer. An example is a PCMCIA slot. The companion system includes a PC card with an interface which mates with the interface on the host. A bus is located on the PC card. A CPU is located on the PC card and connected to the bus. A display is located on the PC card and connected to the bus. An input device is located on the PC card and connected to the bus. A non-volatile memory module is located on the PC card and connected to the bus. A low power random access memory module is located on the PC card and connected to the bus. A power supply is located on the PC card and connected to the components of the PC card. An operating system is located in the non-volatile memory.

**2 Claims, 10 Drawing Sheets**

